



# When 4 channels are not enough ...

DLM4000 Series Mixed Signal Oscilloscope

Bulletin DLM4000-01EN

The DLM4000 is the world's first 8 channel oscilloscope providing comprehensive measurement and analysis capabilities for embedded, automotive, power and mechatronics applications.

Representing decades of experience in providing quality test and measuring tools, the DLM4000 is designed to satisfy the wide ranging needs of engineers today and in the future.

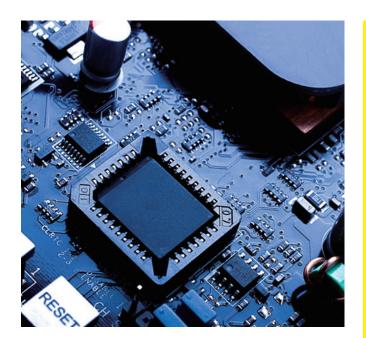
The hardware optimized architecture in the DLM4000 enables measurements and signal processing to be carried out in real time. This means that signals from multiple channels are promptly captured and measurements are always performed and updated at high speed.

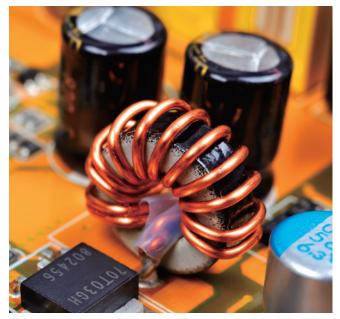
## The DLM4000 is:

Versatile – The number of analog and digital channels, their flexibility and the wealth of measurement and analysis features enable the DLM4000 to solve the broadest range of test requirements.

Intuitive – Via the straightforward interface, users can automatically or manually split the display to separate individual channel waveform while maintaining their full dynamic range. The details of signals can therefore be quickly analyzed irrespective of the number of channels in use.

**Capable** – As intelligent control permeates more and more sectors of the industry from consumer electronics to industrial drives, the signals that engineers need to look at for testing become faster and more complex. The DLM4000 delivers the features and performance that engineers need in an advanced oscilloscope.





# Why choose Yokogawa

#### **Our passion for measurement**

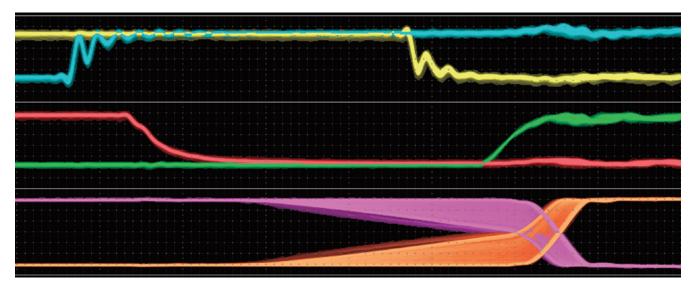
Yokogawa believes that precise and effective measurement lies at the heart of successful innovation – and has focused its own R&D on providing the tools that researchers and engineers need to address their challenges both great and small.

#### **Our heritage**

Yokogawa has been developing measurement solutions for almost 100 years, consistently finding new ways to give R&D teams the tools they need to gain the best insights from their measurement strategies. Our oscilloscope design has been led by customers looking for ease-ofuse and functionality.

#### **Our commitment**

Yokogawa takes pride in its reputation for quality, both in the products we deliver – often adding new features in response to specific client requests – and the level of service and advice we provide to our clients, helping to devise measurement strategies for even the most challenging environments.



# **Superior functionality**

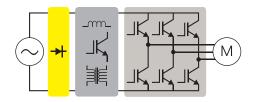
For today's challenges in embedded, automotive, power and mechatronics. The DLM4000 – Eight-channel, 500 MHz bandwidth oscilloscope.

# Motor control & inverter circuit development



The key to efficient and reliable highperformance electric motors is the modern inverter design, or 'Intelligent Power Module'. Multi-channel, high-speed waveform

measurement is an absolute necessity. Four channels are simply not enough. Boasting eight true analog inputs, the DLM4000 empowers today's engineer with a convenient and comprehensive measurement system.



Example: 3 voltage & 3 current measurements of a 3-phase motor Measurement of the gate-drive signals of six IGBTs within the inverter

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## Limitation of 4 ch scope

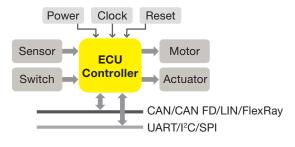
Whole-system measurement is impossible with a fourchannel scope; the real difficulty is measuring the timing between IGBT gate signals within the inverter. Voltage and current measurements between 3 phases and the IO of the motor driver IC is a very challenging test with a fourchannel scope. The truly practical solution is an eightchannel MSO.

# Electronic control unit & mechatronic test



Numerous I/O analog, digital, and serial-bus waveforms surrounding the Electronic Control Unit (ECU) must be measured. The DLM4000 offers ample channel-count and architecture to

monitor eight analog channels and up to 24-bits of logic input while simultaneously performing protocol analysis such as UART, I<sup>2</sup>C, SPI, CAN, CAN FD, LIN and FlexRay. The DLM4000 can speed up the R&D process when four channels are not enough.

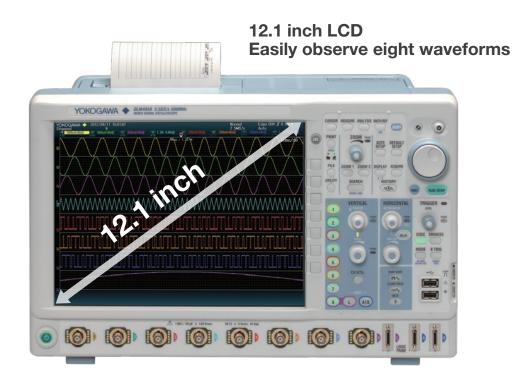


Example: Analog I/O and serial bus controller signals Stringent real time test of digital waveforms in the analog domain.



# Limitation of 4 ch MSO

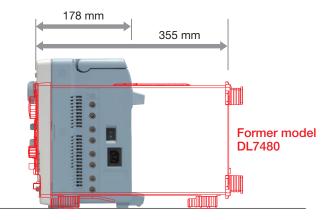
The additional logic inputs of a four-channel MSO mixedsignal oscilloscope provides enough channels, but this method has a blind-spot. Digital waveform analysis using logic inputs alone cannot reveal anomalies such as voltage drift, noise, distortion or ringing, and measure risefall times. ECU testing requires stringent examination of all digital waveforms – and analog input channels are the best tool for the job. <sup>5</sup> The portable eight-channel DLM4000 is the daily instrument of choice.



# Portable



Modest 178 mm depth Half of the former model DL7480



### Long waveform memory Up to 250 MPoints

The two advantages of a long waveform memory are the abilities to capture for long periods of time and to maintain high sample rates. Thus achieving higher effective measuring bandwidths for all time base settings.

<Basic Formula>

Measuring time = Memory length/Sample rate

With the maximum memory installed (/M3 option), in single shot mode, a 10 kHz signal lasting for more than one hour can be captured. The same memory can capture a 200 millisecond signal at a sampling rate of 1.25 GS/s.

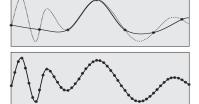
# Relationship between measuring time and sample rate in 250 Mpoint

Sample rate	Maximum measuring time
1.25 GS/s	0.2 s
125 MS/s	2 s
12.5 MS/s	20 s
1.25 MS/s	200 s
125 kS/s	2000 s
62.5 kS/s	5000 s

Caution is needed when using an oscilloscope that does not have enough memory, which can cause lack of sample rate and will possibly fail to capture waveforms accurately.

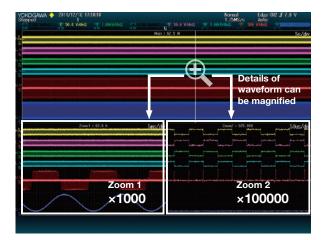
Sample rate is too low.

Sample rate is fairly high.



## Two fully independent zoom windows

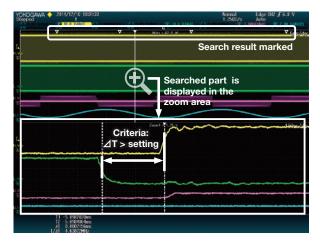
Enabling two fully independent zoom windows allows users to analyze the cause and effect of abnormal behaviors over all input channels. Users can also view and compare the details and timing of different serial buses operating at different speeds.



Detailed waveform measured for 50 seconds are shown in 50 milliseconds and 500 microseconds span.

## Advanced waveform search functions

Single waveform acquisitions of up to 250 MPoints can be searched using various criteria.



Waveform search using "State width"

# 7 History function

# Automatically capture and replay up to 50000 waveforms

The DLM4000 can capture and replay up to 50000 individual acquisitions (/M3 option). These can be displayed one at a time or as an accumulation. Using the search and measurement functions, abnormal signals can therefore be quickly isolated, analyzed and precisely categorized without needing to carefully configure triggers to capture rare events.

# **History search function**

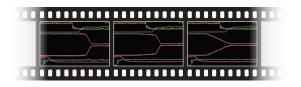
Search up to 50000 waveform history records based on detailed search parameters using the history search function.

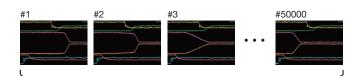


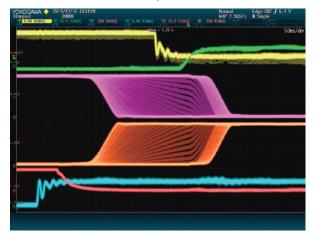
Searching for waveforms that pass through or do not pass through a rectangular zone placed on screen.

# **Replay function**

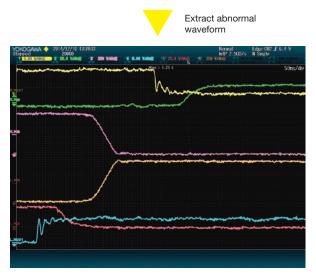
Automatically play back, pause, fast forward, and rewind waveform history records.







Accumulate display mode



Single acquisition display mode

# **Application specific analysis options**

# Serial bus analysis function

## UART (RS232) /I<sup>2</sup>C/SPI/CAN/CAN FD/LIN/FlexRay/SENT/ PSI5/CXPI

Dedicated trigger and analysis options are available for various serial buses of both in-vehicle and embedded systems. A wide variety of trigger combinations can be set, including ID and Data combinations, which can also be combined with conventional edge triggers. (Trigger functions of some of the serial buses are not supported.)

#### Serial bus auto-setup saves time

An intelligent serial bus auto-setup detects bit-rate and voltage threshold automatically and enables the DLM4000 to be quickly configured.

## Up to 4 buses simultaneously

Analysis can be performed at high speed simultaneously on up to four different buses operating at different speeds. This is enhanced by the extensive search facilities, allowing the user to look for specific data in the very long memory. The dual-zoom facility means that different buses can be viewed and debugged alongside each other.

CAN, LIN, SPI, I<sup>2</sup>C can be viewed simultaneously by using zoom.





Four bus decode and list display

#### **Related Accessories**

#### Differential probe PBDH1000 (701924)

DC to1.0 GHz bandwidth1 MΩ, approximately 1.1 pF Maximum differential input voltage range: ±25 V



#### Differential probe (701920)

DC to 500 MHz bandwidth 100 kΩ, approximately 2.5 pF Maximum differential input voltage range: ±12 V



#### Logic probe PBL100/PBL250 (701988/701989)

100 MHz/250 MHz toggle frequency 1 MΩ, 10 pF/100 kΩ, 3 pF



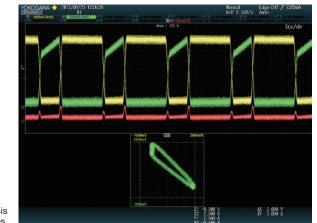
# Power supply analysis function (/G3, /G4 option)

The /G3 and /G4 options enable switching loss, joule integral (I<sup>2</sup>t), SOA (safe operating area), harmonics based on EN61000-3-2, and other power parameters to be measured and analyzed.

## Switching loss analysis

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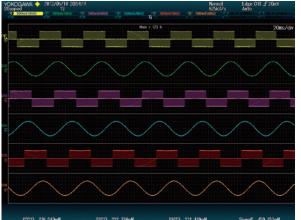
The switching loss of the voltage and current input waveforms can be computed (U(t)  $\times$  I(t)) over long time periods. The turn-on/off loss, the loss including the continuity loss, and the loss over many cycles of the 50 Hz/60 Hz power line can be calculated and analyzed.



Switching loss and SOA analysis of power devices

# **Power measurement**

The DLM4000 can also be used as a power meter by providing automated measurement of power parameters for up to two pairs of voltage and current waveforms, such as the active power, apparent power and power factor. These values can then be statistically processed and calculated.





#### **Related Accessories**

Differential probe PBDH0150 (701927) DC to 150 MHz 1000 Vrms/ ±1400 Vpeak



#### Differential probe (701926)

DC to 50 MHz 5000 Vrms/7000 Vpeak



#### Current probe PBC100/PBC050 (701928/701929)

DC to 100 MHz (701928) DC to 50 MHz (701929) 30 Arms



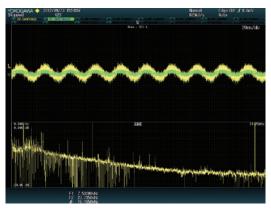
Deskew correction signal source (701936)



# **Features and benefits**

## Waveform computation

The DLM4000 provides powerful and flexible math functions such as arithmetic, filtering and FFT. Up to 4 math channels are available.



FFT analysis of high frequency noise

## Logic signal measurement and analysis <sup>10</sup>

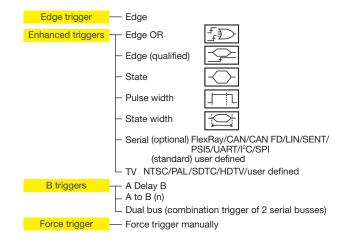
The flexible MSO inputs are included as standard. This enables the DLM4000 to be converted to a 7 analog and 8 digital input MSO. With the /L16 option, up to 24 logic signals can be measured. Bus/State display and optional DA calculation function, which is useful for evaluating AD/ DA converters, are also provided.



Comprehensive waveform display (7 ch + 24 bits)

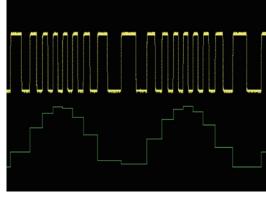
## Reliable triggering

When just a specific event or abnormal waveform needs to be captured, the flexible and reliable triggering of the DLM4000 is the solution. In addition to basic trigger functions such as Edge, State, and Pulse Width – Advanced trigger types are provided, including Edge OR between multiple channels, Serial Bus trigger in which A combination of two bus signals is possible, or an A and B combination of different trigger types.



## User defined math (/G2, /G4 option)

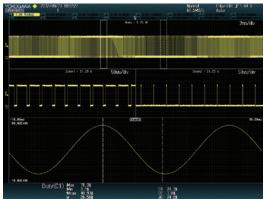
Equations can be arbitrarily created using a suite of operators such as trigonometric and logarithmic operators, integration and differentiation, pulse width operators, phase measurement and digital to analog conversion.



F-V conversion of encoder pulse signal

## <sup>11</sup> Automatic parameter measurement and statistical analysis

30 waveform parameters from a total of 29 different types can be displayed simultaneously with a high update rate. In addition to the basic statistical analysis of repetitively measured parameters, the Yokogawa original "cycle statistic" and "history statistic" measurement functions helps the advanced analysis of periodic mechatronic signals. To observe the fluctuations of measured parameters, it is possible to display them as trends. Period-to-period changes can then be easily seen. The variation of parameters can also be displayed as histograms thus providing a visual method of assessing them statistically.



Trend of waveform parameters

# Variety of display formats

Many types of display format are supported such as split, dual-zoom, XY, FFT, histogram etc.



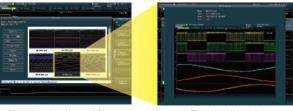
# Automatic GO/NO-GO function

The GO/NO-GO function can be used to test the results of parameter measurements, trigger conditions and other criteria and automatically save or print data, send an e-mail etc. Save time using unattended supervisory data acquisition.



# Thumbnails of saved files

The image and file names are shown so that you can view screen image contents while copying or deleting files. A file can be enlarged to confirm the data.



Thumbnails of saved files

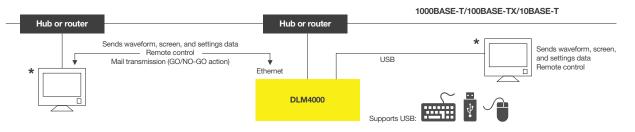
Thumbnail can be viewed full-size

# Built-in user's manual

View detailed graphical explanations of the oscilloscope's functions by pressing the "?" key. Functions and operations can be shown on screen without having to consult the user's manual.

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# PC connectivity and software tools

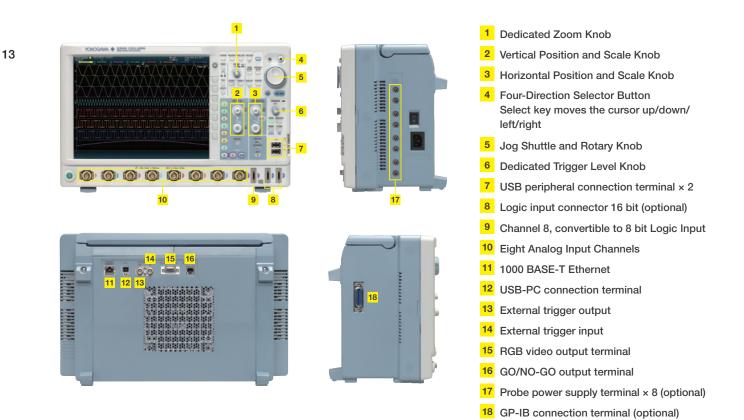


\*DLM4000's internal storage can be recognized by a PC as an external USB storage device. Transferring files is easy even when a USB thumb drive can't be used.

# A comprehensive suite of software tools to support and complement complex measurement tasks.

	Free	Trial version available
Off-line waveform display and analysis	<b>XviewerLITE</b> Basic display and measurement Provides zooming, vertical cursors and data conversion to CSV format.	<b>Xviewer</b> Advanced analysis Xviewer can display acquired waveforms, transfer files and control instruments remotely. In addition to simply displaying the waveform data, Xviewer features many of the same functions that the DLM4000
Waveform monitoring on a PC	<b>Xwirepuller</b> The DLM4000 can be simply controlled using a PC and mouse via an Ethernet, USB, or GP-IB interface. When the software program starts, a	offers; zoom display, cursor measurements, calculation of waveform parameters, complex waveform math and FFT. Binary
Data transfer to a PC	simulation of the oscilloscope appears on the PC display.	waveform data can easily be converted to CSV, Excel or Floating Point Decimal format.
Command control Custom	LabVIEW drivers <sup>*1</sup> By using the LabVIEW driver written for the DLM4000, a developer can dramatically reduce the amount of work required to enable a PC to control the instrument from within the LabVIEW environment. Control libraries The TMCTL DLL (Dynamic Link Library) enables Microsoft Visual studio programs, such as Visual C++ and Visual Basic, to be quickly developed to communicate between the PC and the DLM4000. It supports GPIB, USB and Ethernet interfaces.	
software development	<b>Command line tool</b> The DLTerm command line tool can be used with the TMCTL library to develop communication programs. Prototype code can be rapidly created to automate sequences of capture, measurement and analysis tasks before writing a fully custom software routine.	
	MATLAB*2 WDF Access Toolbox Transfer data file to MATLAB	
	<b>Symbol editor</b> Physical value symbol definition files for CAN and CAN FD serial bus analysis can be created and edited. CANdb files can also be imported.	<ul> <li>*1: Program development environment provided by National Instruments (NI)</li> <li>*2: MathWorks's product</li> </ul>

# **Broad connectivity and easy control**



# **Specifications**

Model name	Frequency band	width		Input	channels	
DLM4038	350 MHz 500 MHz					annels + 8 bit logic
DLM4058			(/L16 option) 8 analog channels + 16 bit logic or 7 analog channels + 24 bit logic			
Analog Sig	nal input					
Input channe	ls					
Analog inp	ut	CH1 to CH8 (CH8 is mutually exclusive with logic input Port L)				
Input couplin	g setting	AC, I	DC, DC50 Ω, GND			
Input impeda	ince					
Analog input		1 ΜΩ 50 Ω			F DC to 500 MHz)	
Voltage axis sensitivity setting range		1 ΜΩ 50 Ω				
Max. input voltage		1 ΜΩ 50 Ω		d 5 Vrms or	10 Vpeak	
Max. DC offset setting range		1 ΜΩ 50 Ω	100 mV/div to 5 1 V/div to 10 V/	600 mV/div div mV/div	±1 V ±10 V ±100 V ±1 V ±5 V	
Vertical-axis (	voltage-axis)					
DC accura	Cy <sup>*1</sup>	±(1.5	5% of 8 div + offset	voltage acc	uracy)	
Offset voltage accuracy*1		100	/ to 50 mV/div mV to 500 mV/div o 10 V/div	±(1% of se	tting + 0.2 mV) tting + 2 mV) tting + 20 mV)	
Frequency ch	naracteristics (-3	dB a	ttenuation when inp	outting a sin	ewave of amplit	ude ±3 div)*1*2
				DLN	14038	DLM4058
1 MΩ (whe		100	mV to 100 V/div	350	) MHz	500 MHz
passive probe)		20 mV to 50 mV/div		300 MHz		400 MHz

50 Ω		10 mV to 500	) mV/div	350 MHz	500 MHz		
		2 mV to 5 m	//div	300 MHz	400 MHz		
Isolation between channels		Maximum bandwidth: -34 dB (typical value)					
Residual noise level	3	The larger of 0.4 mV rms or 0.05 div rms (typical value)					
A/D resolution		8 bit (25 LSB/div) Max. 12 bit (in High Resolution mode			ution mode)		
Bandwidth limit		FULL, 200 MHz, 100 MHz, 20 MHz, 10 MHz, 5 MHz, 2 M 500 kHz, 250 kHz, 125 kHz, 62.5 kHz, 32 kHz, 16 kHz, 8 be set for each channel)					
Maximum sample rate		Real time sar			1.25 GS/s 2.5 GS/s		
		Repetitive sa	mpling mode	125 GS/s			
Maximum record length			Repeat	Single	Single Interleave		
(Points)		Standard	1.25 M	6.25 M	12.5 M		
		/M1	6.25 M	25 M	62.5 M		
		/M2	12.5 M	62.5 M	125 M		
		/M3	25 M	125 M	250 M		
Ch-to-Ch deskew		±100 ns					
Time axis setting ra	nge	1 ns/div to 500 s/div (steps of 1-2-5)					
Time base accuracy	/*1	±0.002%					
Logic Signal Inpu	ıt						
Number of inputs	Standar	d 8 bit × 1 Po	rt L (mutually	exclusive with CH	H8 input)		
	/L16	8 bit × 3 Port L (mutually exclusive with CH8 input), Port A, Port E					
Maximum toggle fre	quency*1	Model 7	01988: 100 N	IHz, Model 7019	89: 250 MHz		
Compatible probes		701988	, 701989 (8 bi	t input) (701980,	701981 are available)		
Min. input voltage		701988	500 mVp-p,	701989: 300 mV	p-p		
Input range		Model 7	01988: ±40 V	, Model 701989:	threshold ±6 V		

#### DLM4000 series

-					J.03 V)	
		Model 701988: $\pm$ 40 V (setting resolution of 0.05 V) Model 701989: $\pm$ 6 V (setting resolution of 0.05 V)				
Input impedance		701988: Approx. 1 ΜΩ/approx. 10 pF 701989: Approx. 100 kΩ/approx. 3 pF				
g rate	1.25 GS/s					
ength (Points)	Standard	Repeat 1.25 M			gle Interleave (A, B) 12.5 M	
	/M1	6.25 M			62.5 M	
	/M2	12.5 M	62.5 M	N	125 M	
		25 M	125 M	N	250 M	
Auto, Auto Leve	el, Normal, Si	ngle, N-Sing	gle			
r source						
Edge	CH1 to CH	8, Logic, E	(T, LINE			
Edge OR	CH1 to CH	8				
Edge Qualified	CH1 to CH	8, Logic, E	π			
State	CH1 to CH	8, Logic				
Pulse width	CH1 to CH	8, Logic, E	π			
State width	CH1 to CH	8, Logic				
TV						
Serial Bus		,				
	UART (opti	onal) CH	H1 to CH8,			
	CAN FD (o	ptional) CH	H1 to CH8			
				Logic		
	PSI5 (optio	nal) CH	H1 to CH8			
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or C	enter/Width a	an be set o	n individual	Channel	s from CH1 to CH8	
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Enabled at 10	0 ms/div to 5	00 s/div (de	pending on	the reco	ord length setting)	
Two zooming	windows car	ı be set inde	pendently (	Zoom1,	Zoom2)	
Zoom factor	×2 to 2	.5 points/10	) div (in zoo	m area)		
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Search function	I <sup>2</sup> C (opt CAN FI	tional), SPI (d D (optional),	optional), UA LIN (option	ART (opti al), FlexF	onal), CAN (optiona Ray (optional),	
				3: 50000		
History search	n Select	Rect, Wave,	Polygon, o	r Parame	eter mode	
Replay function	on Autom	atically displ	ays the hist	ory wave	forms sequentially	
Display	Specifi	ed or averaç	je waveforn	าร		
Types	ΔΤ, ΔV,	ΔΤ & ΔV, Μ	larker, Degr	ee		
	layed wavefo	rm can be r	etained on s	screen		
Currently disp						
	nctions					
<b>d Analysis Fu</b> Max, Min, IntegTY, +	P-P, High, L Over, –Over,	Pulse Cour	nt, Edge Co	unt, V1, V	av, IntegTY+, V2, ΔT, Freq, Perioc η, Duty, Delay	
	Auto, Auto Lew r source Edge Edge OR Edge Qualified State Pulse width State width TV Serial Bus A Delay B A to B(N) Dual Bus Fri range C resolution C acy'1 C or C Tu 12.1 inch TFT Normal, Envel Max. 12 bit (tf by placing a b Real time, inte Select OFF, In Select OFF, In Select OFF, In Select OFF, In Select OFF, In Select OFF, In Search function Search function Search function Max. data (rec Standard: History search Replay function	Standard         /M1         /M2         /M3         Auto, Auto Level, Normal, Si         r source         Edge       CH1 to CH         Edge OR       CH1 to CH         Edge Qualified       CH1 to CH         Edge Qualified       CH1 to CH         Edge Qualified       CH1 to CH         State       CH1 to CH         State       CH1 to CH         State width       CH1 to CH         Serial Bus       PC (options SPI (option UART (opti)         SPI (option SENT (optio	Standard       1.25 M         M1       6.25 M         M2       12.5 M         M3       25 M         Auto, Auto Level, Normal, Single, N-Sing         r source         Edge       CH1 to CH8, Logic, D         Edge Qualified       CH1 to CH8, Logic         Edge Qualified       CH1 to CH8, Logic         Pulse width       CH1 to CH8, Logic         Pulse width       CH1 to CH8, Logic         State       CH1 to CH8, Logic         Pulse width       CH1 to CH8, Logic         Serial Bus       I°C (optional)         FlexRay (optional)       CH         CAN (optional)       CH         CAN (optional)       CH         SENT (optional)       CH         V       CH1 to CH8         Serial Bus       I°C (optional)         CH       CAN (optional)         CH       CH         SENT (optional)       CH         Very Coptional)       CH         Serial Bus only       Force a trigger manually         range       CH1 to CH8       4.0 v from         resolution       CH1 to CH8       4.0 v from         resolution       CH1 to CH8       4.0 v from	Implement         Implement         Implement           Standard         1.25 M         6.25 M           M1         6.25 M         25 N           M2         12.5 M         62.5 N           M3         25 M         125 N           Auto, Auto Level, Normal, Single, N-Single         r           r source         Edge         CH1 to CH8, Logic, EXT, LINE           Edge OR         CH1 to CH8, Logic, EXT         State           Edge Qualified         CH1 to CH8, Logic, EXT         State           State         CH1 to CH8, Logic, EXT         State           State width         CH1 to CH8, Logic, EXT         State           V         CH1 to CH8, Logic, EXT         State           State width         CH1 to CH8, Logic, EXT         State           State         CH1 to CH8, Logic, EXT         State           State         CH1 to CH8, Logic, EXT         State           State width         CH1 to CH8, Logic, EXT	$\begin{tabular}{ c                                   $	

of wave parameters	Up to 2 trend of	or histogram display of specified wave parameters
Computations (MATH)		Delay, Moving Avg, IIR Lowpass, IIR Highpass), Integ, Rotary), user defined math (optional)
Computable no. of traces	4 (Math1 to Ma	ath4)
Max. computable memory length	Standard: 6.25 /M1: 25 MPoin	š MPoints, its, /M2: 62.5 MPoints, /M3: 125 MPoints
Reference function	Up to 4 traces and analyzed	(REF1/REF4) of saved waveform data can be displayed
Action-on-trigger	Actions: Buzze	r, Print, Save, Mail
GO/NO-GO		Nave, Polygon, Parameter r, Print, Save, Mail
XY	Displays XY1, 1	to XY4 and T-Y simultaneously
FFT	Window function	nts: 1.25 k, 12.5 k, 25 k, 125 k, 250 k ons: Rectangular, Hanning, Flat-Top (LS, RS, PSD, CS, TF, CH are available with /G2 or /G4 option)
 Histogram		ogram of acquired waveforms
User-defined math (/G2 and /G4 options)	+, -, ×, /, SIN, LOG, EXP, LN, PWHH, PWLL,	perators can be arbitrarily combined in equations: COS, TAN, ASIN, ACOS, ATAN, INTEG, DIFF, ABS, SQRT, BIN, DELAY, P2 (power of 2), PH, DA, MEAN, HLBT, PWHL, PWLH, PWXX, FV, DUTYH, DUTYL, FILT1, FILT2 record length that can be computed is the same as the functions.
Power supply analysis ( Power analysis	For Pwr1 and I	tions) Pwr2, selectable from 4 analysis types. Deskweing between d current waveforms can be executed automatically.
	Switching loss	Measurement of total loss and switching loss, power waveform display, Automatic measurement and statistical analysis of power analysis items (Wp, Wp+, Wp–, Abs. Wp, P, P+, P–, Abs.P, Z)
	Safety operation area	SOA analysis by X-Y display, using voltage as X axis, and current as Y axis is possible
	Harmonic analysis	Basic comparison is possible with following standard Harmonic emission standard IEC61000-3-2 edition 2.2, EN61000-3-2 (2000), IEC61000-4-7 edition 2
	Joule integral	Joule integral (I <sup>2</sup> t) waveform display, automatic measurement and statistical analysis is possible
Power Measurement		asurement of power parameters for up to four pairs of voltage veforms. Values can be statistically processed and calculated
	Measurement parameters	Urms, Umn, Udc, Urmn, Uac, U+pk, U-pk, Up-p, Irms, Imn, Idc, Irmn, Iac, I+pk, I-pk, Ip-p, P, S, Q, Z, A, Wp, Wp+ Wp-, Abs.Wp, q, q+, q-, Abs.q, Avg Freq (voltage, current)
Common Features of	of Serial Bus Si	gnal Analysis Functions (/F1 to /F11 Options)
Analysis result display	Decoded in	formation is displayed together with waveforms or in list form
Auto setup function	bus-specifi automatica detected re	I value, time axis scale, voltage axis scale and other c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.)
Auto setup function	bus-specifi automatica detected re (The type o Search of a	c parameters such as a bit rate and recessive level are Ily detected. Trigger conditions are set based on the sult and decoded information is displayed.
Search function	bus-specifi automatica detected re (The type o Search of a condition s	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) ill waveforms for a position that matches a pattern or
Search function Analysis result saving function	bus-specifi automatica detected re (The type o Search of a condition s Analysis list also saved	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) all waveforms for a position that matches a pattern or pecified by data information.
Search function Analysis result saving function I <sup>2</sup> C Bus Signal Analy	bus-specifi automatica detected re (The type o Search of a condition s Analysis list also saved <b>isis Functions</b>	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) all waveforms for a position that matches a pattern or pecified by data information. t data can be saved to CSV-format files. Trend data can be for SENT signals. (/F2 and /F3 Options) ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit
Search function Analysis result saving function I <sup>2</sup> C Bus Signal Analy Applicable bus	bus-specifi automatica detected re (The type o Search of a condition s Analysis list also saved sis Functions [ <sup>P</sup> C bus Bus t SM bus Com	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) ill waveforms for a position that matches a pattern or pecified by data information. it data can be saved to CSV-format files. Trend data can be for SENT signals. (/F2 and /F3 Options) ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit plies with System Management Bus
Search function Analysis result saving function I <sup>2</sup> C Bus Signal Analy Applicable bus Analyzable signals	bus-specifi automatica detected re (The type o Search of a condition s Analysis list also saved is Functions (I <sup>2</sup> C bus Bus t SM bus Comp CH1 to CH8, L	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) ill waveforms for a position that matches a pattern or pecified by data information. it data can be saved to CSV-format files. Trend data can be for SENT signals. (/F2 and /F3 Options) ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit piles with System Management Bus .ogic input, or M1 to M4
Search function Analysis result saving function I <sup>2</sup> C Bus Signal Analy Applicable bus Analyzable signals I <sup>2</sup> C Trigger modes	bus-specifi automatica detected re (The type o Search of a condition s Analysis list also saved FC bus Bus t SM bus Comp CH1 to CH8, L Every Start, Ac	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) Ill waveforms for a position that matches a pattern or pecified by data information. data can be saved to CSV-format files. Trend data can be for SENT signals. ( <b>/F2 and /F3 Options)</b> ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit plies with System Management Bus .ogic input, or M1 to M4 Idress & Data, Non-Ack, General Call, Start Byte, HS Mode
Search function Analysis result saving function IPC Bus Signal Analy Applicable bus Analyzable signals IPC Trigger modes Analyzable no. of data	bus-specifi automatica detected re (The type of Search of a condition s Analysis list also saved sis Functions [ <sup>7</sup> C bus Bus t SM bus Com CH1 to CH8, L Every Start, Ac 300000 bytes Analysis no., tit	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) Ill waveforms for a position that matches a pattern or pecified by data information. data can be saved to CSV-format files. Trend data can be for SENT signals. ( <b>/F2 and /F3 Options)</b> ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit plies with System Management Bus .ogic input, or M1 to M4 Idress & Data, Non-Ack, General Call, Start Byte, HS Mode
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Search function Analysis result saving function I <sup>2</sup> C Bus Signal Analy Applicable bus Analyzable signals I <sup>2</sup> C Trigger modes Analyzable no. of data List display items SPI Bus Signal Analy	bus-specifi automatica detected re (The type o Search of a condition s Analysis list also saved sis Functions FC bus Bus t SM bus Comp CH1 to CH8, L Every Start, Ac 300000 bytes Analysis no., ti byte address, I ysis Functions 3 wire, 4 wi	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) all waveforms for a position that matches a pattern or pecified by data information. t data can be saved to CSV-format files. Trend data can be for SENT signals. (/F2 and /F3 Options) ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit plies with System Management Bus logic input, or M1 to M4 tdress & Data, Non-Ack, General Call, Start Byte, HS Mode max. me from trigger position (Time (ms)),1st byte address, 2nd RW, Data, Presence/absence of ACK, information (/F2 and /F3 Options) ire
Search function Analysis result saving function I°C Bus Signal Analy Applicable bus Analyzable signals I°C Trigger modes Analyzable no. of data List display items SPI Bus Signal Analy Trigger types	bus-specifi automatica detected re (The type o Search of a condition s Analysis list also saved sis Functions (PC bus Bus t SM bus Com CH1 to CH8, L Every Start, Ac 300000 bytes Analysis no., ti byte address, I ysis Functions 3 wire, 4 wi After assert	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) all waveforms for a position that matches a pattern or pecified by data information. t data can be saved to CSV-format files. Trend data can be for SENT signals. (/F2 and /F3 Options) ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit plies with System Management Bus .ogic input, or M1 to M4 idress & Data, Non-Ack, General Call, Start Byte, HS Mode max. me from trigger position (Time (ms)),1st byte address, 2nd RVW, Data, Presence/absence of ACK, information (/F2 and /F3 Options) ire on of CS, compares data after arbitrary byte count and triggers.
Search function Analysis result saving function I <sup>2</sup> C Bus Signal Analy Applicable bus Analyzable signals I <sup>2</sup> C Trigger modes Analyzable no. of data List display items	bus-specifi automatica detected re (The type o Search of a condition s Analysis list also saved sis Functions (PC bus Bus t SM bus Com CH1 to CH8, L Every Start, Ac 300000 bytes Analysis no., ti byte address, I ysis Functions 3 wire, 4 wi After assert	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) all waveforms for a position that matches a pattern or pecified by data information. t data can be saved to CSV-format files. Trend data can be for SENT signals. (/F2 and /F3 Options) ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit plies with System Management Bus logic input, or M1 to M4 tdress & Data, Non-Ack, General Call, Start Byte, HS Mode max. me from trigger position (Time (ms)),1st byte address, 2nd RW, Data, Presence/absence of ACK, information (/F2 and /F3 Options) ire
Search function Analysis result saving function I*C Bus Signal Analy Applicable bus Analyzable signals I*C Trigger modes Analyzable no. of data List display items SPI Bus Signal Analy Trigger types Analyzable signals	bus-specifi automatica detected re (The type o Search of a condition s Analysis list also saved sis Functions (FC bus Bus t SM bus Com CH1 to CH8, L Every Start, Ac 300000 bytes Analysis no., ti byte address, I sysis Functions 3 wire, 4 wi After asserti CH1 to CH SMS, LSB	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) all waveforms for a position that matches a pattern or pecified by data information. t data can be saved to CSV-format files. Trend data can be for SENT signals. (/F2 and /F3 Options) ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit plies with System Management Bus .ogic input, or M1 to M4 idress & Data, Non-Ack, General Call, Start Byte, HS Mode max. me from trigger position (Time (ms)),1st byte address, 2nd RVW, Data, Presence/absence of ACK, information (/F2 and /F3 Options) ire on of CS, compares data after arbitrary byte count and triggers.
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Search function Analysis result saving function I <sup>2</sup> C Bus Signal Analy Applicable bus Analyzable signals I <sup>2</sup> C Trigger modes Analyzable no. of data List display items SPI Bus Signal Analy Trigger types Analyzable signals Byte order Field definition	bus-specifi automatica detected re (The type o Search of a condition s Analysis list also saved sis Functions (PC bus Bus t SM bus Comp CH1 to CH8, L Every Start, Ac 300000 bytes Analysis no., tii byte address, I ysis Functions 3 wire, 4 wi After asserti CH1 to CH MSB, LSB Field size (4 300000 bytes	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) ill waveforms for a position that matches a pattern or pecified by data information. it data can be saved to CSV-format files. Trend data can be for SENT signals. (/F2 and /F3 Options) ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit plies with System Management Bus cogic input, or M1 to M4 Idress & Data, Non-Ack, General Call, Start Byte, HS Mode max. me from trigger position (Time (ms)),1st byte address, 2nd R/W, Data, Presence/absence of ACK, information (/F2 and /F3 Options) ire on of CS, compares data after arbitrary byte count and triggers. 8, Logic input, M1 to M4
Search function Analysis result saving function I <sup>2</sup> C Bus Signal Analy Applicable bus Analyzable signals I <sup>2</sup> C Trigger modes Analyzable no. of data List display items SPI Bus Signal Analy Trigger types Analyzable signals Byte order Field definition Analyzable no. of data	bus-specifi automatica detected re (The type o Search of a condition s Analysis list also saved sis Functions (CH1 to CH8, L Every Start, Ac 300000 bytes Analysis no., tii byte address, I ysis Functions 3 wire, 4 wi After asserti CH1 to CH MSB, LSB Field size (4 300000 bytes Analysis no.	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) ill waveforms for a position that matches a pattern or pecified by data information. it data can be saved to CSV-format files. Trend data can be for SENT signals. (/F2 and /F3 Options) ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit plies with System Management Bus cogic input, or M1 to M4 idress & Data, Non-Ack, General Call, Start Byte, HS Mode max. me from trigger position (Time (ms)), 1st byte address, 2nd R/W, Data, Presence/absence of ACK, information (/F2 and /F3 Options) ire on of CS, compares data after arbitrary byte count and triggers. 8, Logic input, M1 to M4 it to 32 bits), Enabled bit range tes max. ., time from trigger position (Time (ms)), Data 1, Data 2
Search function Analysis result saving function I*C Bus Signal Analy Applicable bus Analyzable signals I*C Trigger modes Analyzable no. of data List display items SPI Bus Signal Analy Trigger types Analyzable signals Byte order Field definition Analyzable no. of data List display items	bus-specifi automatica detected re (The type o Search of a condition si also saved sis Functions (PC bus Bus t SM bus Comp CH1 to CH8, L Every Start, Ac 300000 bytes Analysis no., th byte address, I ysis Functions 3 wire, 4 wi After asserti CH1 to CH MSB, LSB Field size (4 300000 bytes Field size (4 300000 bytes Fiel	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) ill waveforms for a position that matches a pattern or pecified by data information. it data can be saved to CSV-format files. Trend data can be for SENT signals. (/F2 and /F3 Options) ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit plies with System Management Bus cogic input, or M1 to M4 idress & Data, Non-Ack, General Call, Start Byte, HS Mode max. me from trigger position (Time (ms)), 1st byte address, 2nd R/W, Data, Presence/absence of ACK, information (/F2 and /F3 Options) ire on of CS, compares data after arbitrary byte count and triggers. 8, Logic input, M1 to M4 it to 32 bits), Enabled bit range tes max. ., time from trigger position (Time (ms)), Data 1, Data 2
Search function Analysis result saving function I <sup>2</sup> C Bus Signal Analy Applicable bus Analyzable signals I <sup>2</sup> C Trigger modes Analyzable no. of data List display items SPI Bus Signal Analy Trigger types Analyzable signals Byte order Field definition Analyzable no. of data List display items UART Signal Analysi	bus-specifi automatica detected re (The type o Search of a condition s Analysis list also saved sis Functions (PC bus Bus t SM bus Comp CH1 to CH8, L Every Start, Ac 300000 bytes Analysis no., ti byte address, I ysis Functions 3 wire, 4 wi After asserti CH1 to CH MSB, LSB Field size (4 300000 bytes Field size (4 300000 bytes Field size (4 300000 bytes Analysis no.	c parameters such as a bit rate and recessive level are lly detected. Trigger conditions are set based on the sult and decoded information is displayed. f a bus signal needs to be specified in advance.) all waveforms for a position that matches a pattern or pecified by data information. t data can be saved to CSV-format files. Trend data can be for SENT signals. (/F2 and /F3 Options) ransfer rate: 3.4 Mbit/s max. Address mode: 7 bit/10 bit plies with System Management Bus .ogic input, or M1 to M4 tdress & Data, Non-Ack, General Call, Start Byte, HS Mode max. me from trigger position (Time (ms)), 1st byte address, 2nd RW, Data, Presence/absence of ACK, information (/F2 and /F3 Options) ire on of CS, compares data after arbitrary byte count and triggers. 8, Logic input, M1 to M4 to 32 bits), Enabled bit range tes max. ., time from trigger position (Time (ms)), Data 1, Data 2 F1 and /F3 Options) pops, 57600 bps, 38400 bps, 19200 bps, 9600 bps, s, 2400 bps, 1200 bps, User Define (an arbitrary bit rate



	odes	Every Data, Data, Error (Framing, Parity)
Analyzable no. of frames		300000 frames max.
List display items	3	Analysis no., time from trigger position (Time(ms)), Data (Bin, Hex) display, ASCII display, and Information.
	al Analysi	s Functions (/F4, /F6, /F7 and /F8 Options)
Applicable bus		CAN version 2.0A/B, Hi-Speed CAN (ISO11898), Low-Speed CAN (ISO11519-2)
Analyzable signa	ls	CH1 to CH8, M1 to M4
Bit rate		1 Mbps, 500 kbps, 250 kbps, 125 kbps, 83.3 kbps, 33.3 kbps User Define (an arbitrary bit rate from 10 kbps to 1 Mbps with resolution of 100 bps)
CAN bus Trigger	modes	SOF, ID/Data, ID OR, Error (Error Frame, Stuff, CRC), Message and signal (enabled when loading physical values/symbol definitions)
Analyzable no. o	f frames	100000 frames max.
List display items	3	Analysis no., time from trigger position (Time (ms)), Frame type, ID, DLC, Data, CRC, presence/absence of Ack, information
Auxiliary analysis	functions	Field jump functions
	Signal Ana	lysis Functions (/F7 and /F8 Options)
Applicable bus		CAN FD (ISO 11898-1:2015 and non-ISO)
Analyzable signa		CH1 to CH8, M1 to M4
Bit rate	Arbitration	20 kbps to 1 Mbps with resolution of 100 bps)
	Data	8 Mbps, 5 Mbps, 4 Mbps, 2 Mbps, 1 Mbps, 500 kbps, User Define (an arbitrary bit rate from 250 kbps to10 Mbps with resolution of 100 bps)
CAN FD bus Trig	Iger modes	SOF, ID/DATA, ID OR, FDF, ESI, Error (Error Frame, Stuff, Fixed Stuff, CRC), Message and signal (enabled when loading physical values/ symbol definitions)
Analyzable no. o	f frames	50000 frames max.
List display items	3	Analysis no., time from trigger position (Time (ms)), Frame type, ID, DLC, Data, CRC, presence/absence of Ack, information
Auxiliary analysis	functions	Field jump functions
LIN Bus Signa	I Analysis	Functions (/F4, /F6, /F7 and /F8 Options)
Applicable bus		LIN Rev. 1.3, 2.0, 2.1
Analyzable signals		CH1 to CH8, M1 to M4
Bit rate		19.2 kbps, 9.6 kbps, 4.8 kbps, 2.4 kbps, 1.2 kbps User Define (an arbitrary bit rate from 1 kbps to 20 kbps with resolution of 10 bps)
LIN bus Trigger r	nodes	Break Synch, ID/Data, ID OR, and Error trigger
Analyzable no. o	f frames	100000 frames max.
List display items	3	Analysis no., time from trigger position (Time (ms)), ID, ID-Field, Data, CheckSum, information
	functions	Field jump functions
Auxiliary analysis		
· · ·	nal Analys	is Functions (/F4, /F6, /F7 and /F8 Options) <sup>5</sup>
CXPI Bus Sign		is Functions (/F4, /F6, /F7 and /F8 Options)' <sup>5</sup> CXPI JASO D 015-3:2015
CXPI Bus Sigr	lard	CXPI JASO D 015-3:2015 CH1 to CH8, M1 to M4
CXPI Bus Sigr Applicable stand Analyzable signa	lard	CXPI JASO D 015-3:2015
CXPI Bus Sigr Applicable stand Analyzable signa Bit rate	lard Ils	CXPI JASO D 015-3:2015 CH1 to CH8, M1 to M4 19.2 kbps, 9.6 kbps, 4.8 kbps, User Define (an arbitrary bit rate from
CXPI Bus Sign Applicable stand Analyzable signa Bit rate Analyzable no. o List display items	lard Ils f frames s	CXPI JASO D 015-3:2015 CH1 to CH8, M1 to M4 19.2 kbps, 9.6 kbps, 4.8 kbps, User Define (an arbitrary bit rate from 4 kbps to 50 kbps with resolution of 10 bps) 10000 frames max. Analysis no., time from trigger position (Time (ms)), ID, DLC, W/S, CT Data, CRC, error information, Wakeup/Sleep
CXPI Bus Sign Applicable stand Analyzable signa Bit rate Analyzable no. o List display items FlexRay Bus S	lard Ils f frames s	CXPI JASO D 015-3:2015 CH1 to CH8, M1 to M4 19.2 kbps, 9.6 kbps, 4.8 kbps, User Define (an arbitrary bit rate from 4 kbps to 50 kbps with resolution of 10 bps) 10000 frames max. Analysis no., time from trigger position (Time (ms)), ID, DLC, W/S, CT Data, CRC, error information, Wakeup/Sleep Ilysis Functions (/F5, /F6 and /F8 Options)
CXPI Bus Sigr Applicable stand Analyzable signa Bit rate Analyzable no. o List display items FlexRay Bus S Applicable bus	lard Ils f frames s Signal Ana	CXPI JASO D 015-3:2015 CH1 to CH8, M1 to M4 19.2 kbps, 9.6 kbps, 4.8 kbps, User Define (an arbitrary bit rate from 4 kbps to 50 kbps with resolution of 10 bps) 10000 frames max. Analysis no., time from trigger position (Time (ms)), ID, DLC, W/S, CT Data, CRC, error information, Wakeup/Sleep Ilysis Functions (/F5, /F6 and /F8 Options) FlexRay Protocol Version 2.1
CXPI Bus Sigr Applicable stand Analyzable signa Bit rate Analyzable no. o List display items FlexRay Bus S Applicable bus Analyzable signa	lard Ils f frames s Signal Ana	CXPI JASO D 015-3:2015 CH1 to CH8, M1 to M4 19.2 kbps, 9.6 kbps, 4.8 kbps, User Define (an arbitrary bit rate from 4 kbps to 50 kbps with resolution of 10 bps) 10000 frames max. Analysis no., time from trigger position (Time (ms)), ID, DLC, W/S, CT Data, CRC, error information, Wakeup/Sleep Ilysis Functions (/F5, /F6 and /F8 Options) FlexRay Protocol Version 2.1 CH1 to CH8, M1 to M4
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List display items Fast channel	Analysis no., time from trigger position (Time (ms)), Sync/Cal period,
	Tick, Status & Comm, Data, CRC, frame length, information
Slow channel	Analysis no., time from trigger position (Time (ms)), ID, Data, CRC, information
Auxiliary analysis functions	Trend functions (up to 4 trend waveforms)
PSI5 Signal Analysis Fund	ctions (/F10 and /F11 Options)
Applicable standard	PSI5 Airbag (V2.2) <sup>16</sup>
Analyzable signals	CH1 to CH8, M1 to M4
Bit rate	189 kbps, 125 kbps, User Define (10.0 k to 1000.0 kbps, with resolution of 0.1 kbps)
PSI5 Trigger modes	Sync, Start Bit, Data
Analyzable no. of frames	400000 frames max.
List display items	Analysis no., time from trigger position, time from Sync, slot no., Data, Parity/CRC, Information
Auxliary analysis function	Trend functions (up to 4 trend waveforms)
GP-IB (/C1 Option)	
Electromechanical specificati	ons Conforms to IEEE std. 488-1978 (JIS C 1901-1987)
Protocol	Conforms to IEEE std. 488.2-1992
Auxiliary Input	
Rear panel I/O signal	External trigger input/output, GO/NO-GO output, video output
Probe interface terminal	8 terminals (front panel)
Probe power terminal	8 terminals (side panel), (/P8 option)
Internal Storage (Standar	
Capacity	Standard: Approx. 1.8 GB, /C8 option: Approx. 7.2 GB
Built-in Printer (/B5 Optio	n)
Built-in printer	112 mm wide, monochrome, thermal
USB Peripheral Connecti	on Terminal
Connector	USB type A connector × 2 (front panel)
Electromechanical specificati	ons USB 2.0 compliant
Supported transfer standards	s Low Speed, Full Speed, High Speed
Supported devices	USB Mass Storage Class Ver. 1.1 compliant mass storage devices USB HID Class Ver.1.1 compliant mouse, keyboard
USB-PC Connection Terr	ninal
Connector	USB type B connector × 1
Electromechanical specificati	ons USB 2.0 compliant
Supported transfer standards	s High Speed, Full Speed
Supported class	USBTMC-USB488 (USB Test and Measurement Class Ver. 1.0)
Ethernet	· · ·
Connector RJ-	45 connector × 1
Transmission methods Ethe	ernet (1000BASE-T/100BASE-TX/10BASE-T)
Supported services Ser	ver: FTP, HTTP, VXI-11 Client: FTP, SMTP, SNTP, LPR, DHCP, DNS
General Specifications	
Rated supply voltage	100 to 240 VAC
Rated supply frequency	50 Hz/60 Hz
Maximum power consumptio	on 250 VA (when printer is used)
External dimensions	426 (W) × 266 (H) × 178 (D) mm (when printer cover is closed, excluding protrusions)
Weight	Approx. 6.6 kg, With no options
Operating temperature range	
*1 Measured under standard operati Standard operating conditions: A Error in supply voltage and freque 2 Value in the case of repetitive phe two values, DC to sampling frequ '3 When the input section is shorted, if 4 The LCD may include a few defectiv	ing conditions after a 30-minute warm-up followed by calibration. mbient temperature: $230 \pm 5C$ . Ambient humidity: $55 \pm 10\%$ RH ano; Within 1% of rating momenon. The frequency bandwidth of a single-shot phenomenon is the smaller of the ency/2.5 or the frequency bandwidth of the repetitive phenomenon. The frequency bandwidth of the repetitive phenomenon. a counsidian of the probate set to Narma, accumulation is OFF, and the proba attenuation is set to 1 to pixels (within 4 ppm over the total number of pixels including RGB). Beas contact our sales representative.

External dimensions

#### Model and Suffix code

Model	Suffix co <u>de</u>	Description
DLM4038*1		Mixed Signal Oscilloscope: 8 ch, 350 MHz
DLM4058 <sup>-1</sup>		Mixed Signal Oscilloscope: 8 ch, 500 MHz
Power cord	I -D	UL/CSA standard
	-F	VDE standard
	-Q	BS standard
-R -H -N		AS standard
		GB standard
		NBR standard
	-T	Taiwanese Standard
	-B	Indian Standard
	-U	IEC Plug Type B
Language	-HE	English Message and Panel
Language	-HC	Chinese Message and Panel
	-HK	Korean Message and Panel
	-HG	German Message and Panel
	-HG -HF	French Message and Panel
	-HL	Italian Message and Panel
	-HL -HS	Spanish Message and Panel
Option	/L16	Logic 16bit
option _	/L16 /B5	Built-in printer (112 mm)
-	/60	Memory expansion
	/M1*2	During continuous measurement: 6.25 Mpoints;
		Single mode: 25 Mpoints (when interleave mode ON: 62.5 Mpoints)
		Memory expansion
	/M2 <sup>*2</sup>	During continuous measurement: 12.5 Mpoints;
	/1012	Single mode: 62.5 Mpoints (when interleave mode ON: 125 Mpoints)
		Memory expansion
	/M3 <sup>*2</sup>	During continuous measurement: 25 Mpoints;
		Single mode: 125 Mpoints (when interleave mode ON: 250 Mpoints)
-	/P8*3	Eight probe power connectors
_	/C1	GP-IB Interface
_	/C8	Internal storage (7.2 GB)
-	/G2*4	User defined math
	/G3*4	Power supply analysis function
	/G4 <sup>*4</sup>	Power supply analysis function (includes /G2)
-	/F1 <sup>*5</sup>	UART trigger and analysis
	/F2*5	I <sup>2</sup> C + SPI trigger and analysis
	/F3*5	UART + I <sup>2</sup> C + SPI trigger and analysis
-	/F4*6	CAN + LIN trigger and analysis + CXPI analysis <sup>12</sup>
	/F5 <sup>°6</sup>	FlexRay trigger and analysis
	/F6 <sup>°6</sup>	CAN + LIN + FlexRay trigger and analysis + CXPI analysis <sup>12</sup>
	/F7*6	CAN + CAN FD + LIN trigger and analysis + CXPI analysis
	/F8 <sup>*6</sup>	CAN+CAN FD+LIN + FlexRay trigger and analysis + CXPI analysis
-	/F0*7	SENT trigger and analysis
	/F9 <sup>-</sup> /F10 <sup>-7</sup>	PSI5 analysis
	/F107 /F1177	
-	/F11/ /E1 <sup>*8</sup>	SENT + PSI5 trigger and analysis
	/E1 ° /E2*8*9	Four additional 701939 probes (8 in total)
	/E2 ° ° /E3*8*9	Attach four 701946 probes Attach eight 701946 probes

#### Standard Main Unit Accessories

Power cord (1 set), Passive probe 701939 (500 MHz, 1.3 m)<sup>-10</sup> 4 set, Protective front cover (1 set), Soft carrying case for probes (1 set), Printer roll paper (for /B5 option) 1 roll, Rubber leg cap (1 set), User's manuals<sup>11</sup>

- \*1: Logic probes are not included. Please order the accessory logic probe 701988/701989 sold separately.

- Clogic probes are not included. Please order the accessory logic probe /01969/01969 sold separately.
   Chylone from the each note can be selected at a time.
   Specify this option when using current probes or differential probes that don't support probe interface.
   to '8: Only one from the each note can be selected at a time.
   The 701939 probes are not included when this option is selected.
   When /E1 option is selected, eight 701939 probes are included. When either /E2 or /E3 option is selected, are 701909 probes included. no 701939 probe is included.
- \*11: Start guide as the printed material, and User's manuals as CD-ROM are included
- \*12: If the trigger function is required, please contact our sales representative

#### NOTICE -

• Before operating the product, read the user's manual thoroughly for proper and safe operation

This is a Class A instrument based on Emission standards EN61326-1 and EN55011, and is designed for an industrial environment.

Operation of this equipment in a residential area may cause radio interference, in which case users will be responsible for any interference which they cause.

# YOKOGAWA

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#### Additional Option License for DLM4000<sup>\*1</sup>

Model Suffix code Description

mouor	Cullix Couc	Decemption
709820	-G2	User defined math
	-G3	Power supply analysis function
-G4 -F1		Power supply analysis function (includes G2)
		UART trigger and analysis
	-F2	I <sup>2</sup> C + SPI trigger and analysis
	-F3	UART + I <sup>2</sup> C + SPI trigger and analysis
	-F4	CAN + LIN trigger and analysis + CXPI analysis <sup>2</sup>
	-F5	FlexRay trigger and analysis
	-F6	CAN + LIN + FlexRay trigger and analysis + CXPI analysis <sup>2</sup>
	-F7	CAN + CAN FD + LIN trigger and analysis + CXPI analysis <sup>2</sup>
	-F8	CAN + CAN FD + LIN + FlexRay trigger and analysis + CXPI analysis <sup>2</sup>
	-F9	SENT trigger and analysis
	-10	PSI5 analysis
	-11	SENT+PSI5 trigger and analysis
	-X1	F4 -> F7/F6 -> F8 (add CAN FD)

\*1: Separately sold license product (customer-installable). \*2: If the trigger function is required, please contact our sales representative.

#### Accessories (sold separately)

Model	Product	Description
701988	Logic probe (PBL100)	1 MΩ, 100 MHz, 8 inputs
701989	Logic probe (PBL250)	100 kΩ, 250 MHz, 8 inputs
701939	Passive probe <sup>*1</sup>	10 MΩ (10:1), 500 MHz, 1.3 m
701946	Miniature passive probe	10 MΩ (10:1), 500 MHz, 1.2 m
702906	Passive probe (wide temperature range)	10 MΩ (10:1), 200 MHz, 2.5 m -40°C to 85°C
700939	FET probe <sup>1</sup>	900 MHz bandwidth, 2.5 MΩ (10:1), 1.8 pF
701944	100:1 high voltage probe	400 MHz bandwidth, 1.2 m, 1000 Vrms
701945	100:1 high voltage probe	250 MHz bandwidth, 3 m, 1000 Vrms
701924	Differential probe (PBDH1000)	1 GHz bandwidth, 1 M $\Omega$ (50:1), max. ±25 V
701927	Differential probe (PBDH0150)	150 MHz bandwidth, max. ±1400 V, 1 m extension lead
701920	500 MHz differential probe	500 MHz bandwidth, max. ±12 V
701922	200 MHz differential probe	200 MHz bandwidth, max. ±20 V
700924	100 MHz differential probe	100 MHz bandwidth, max. ±1400 V
701921	100 MHz differential probe	100 MHz bandwidth, max. ±700 V
701926	50 MHz differential probe	50 MHz bandwidth, max. 5000 Vrms
700925	15 MHz differential probe	15 MHz bandwidth, max. ±500 V
701917	Current probe (High-sensitivity) <sup>2</sup>	50 MHz bandwidth, max. 5 Arms
701918	Current probe (High-sensitivity) <sup>2</sup>	120 MHz bandwidth, max. 5 Arms
701928	Current probe (PBC100) <sup>-2</sup>	100 MHz bandwidth, max. 30 Arms
701929	Current probe (PBC050) <sup>-2</sup>	50 MHz bandwidth, max. 30 Arms
701930	Current probe <sup>-2</sup>	10 MHz bandwidth, max. 150 Arms
701931	Current probe <sup>2</sup>	2 MHz bandwidth, max. 500 Arms
701936	Deskew correction signal source	For deskew between voltage and current
701919	Probe stand	Round base, 1 arm
B9988AE	Printer roll paper	One lot: 10 rolls, 10 m each
366973	GO/NO-GO cable	GO/NO-GO signal output
701968	Soft carrying case	For DLM4000
701969-E	Rack mount kit for DLM4000	EIA standard-compliant
701969-J	Rack mount kit for DLM4000	JIS standard-compliant

\*2: Current probes' maximum input current may be limited by the number of the probes used at a time.

#### **Accessory Software**

Model	Product	Description
701992-SP01	-Xviewer	Viewer software (standard edition)
701992-GP01	- Xviewer	Viewer software (MATH edition)

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- · Yokogawa's electrical products are developed and produced in facilities that have received ISO14001 approval.
- . In order to protect the global environment, Yokogawa's electrical products are designed in accordance with Yokogawa's Environmentally Friendly Product Design Guidelines and Product Design Assessment Criteria.

#### https://tmi.yokogawa.com/

#### YMI-KS-MI-SE07

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